

JTAGICE3

The Atmel JTAGICE3 is a powerful development tool for on-chip debugging and programming of all AVR® devices. It supports:

- Programming and on-chip debugging of all Atmel AVR 32-bit microcontrollers on both JTAG and aWire interfaces.
- Programming and on-chip debugging of all Atmel AVR XMEGA family devices on both JTAG and PDI 2-wire interfaces.
- Programming (JTAG and SPI) and debugging of all Atmel AVR 8-bit microcontrollers with OCD support on both JTAG or debugWIRE interfaces.



- Fully compatible with Atmel Studio
- Supports programming and debugging of all Atmel AVR UC3 32-bit microcontrollers
- Supports programming and debugging of all 8-bit AVR XMEGA devices
- Supports programming and debugging of all 8-bit Atmel megaAVR and tinyAVR devices with OCD
- Target operating voltage range of 1.65V to 5.5V
- Draws less than 1mA from target VTref during operation
- Supports JTAG clock frequencies from 32kHz to 15MHz
- Supports PDI clock frequencies from 32kHz to 10MHz
- Supports debugWIRE baud rates from 4 kbit/s to 0.5 Mbit/s
- Supports aWire baud rates from 7.5 kbit/s to 7.5 Mbit/s
- Supports SPI clock frequencies from 8 kHz to 1.875 MHz
- USB 2.0 high-speed host interface
- Supports 10-pin 50-mil JTAG connector, as well as 10-pin 100mil JTAG, 6-pin 50-mil SPI and 6-pin 100-mil SPI interfaces using adapters.

